FIG. 1

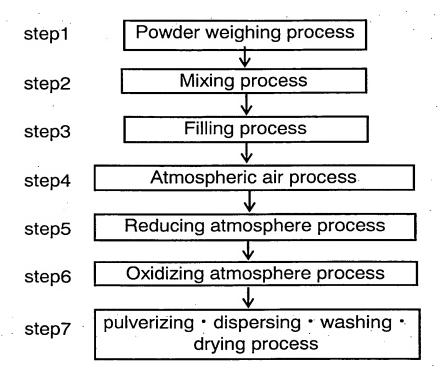
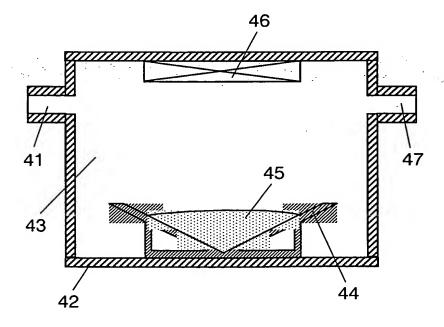
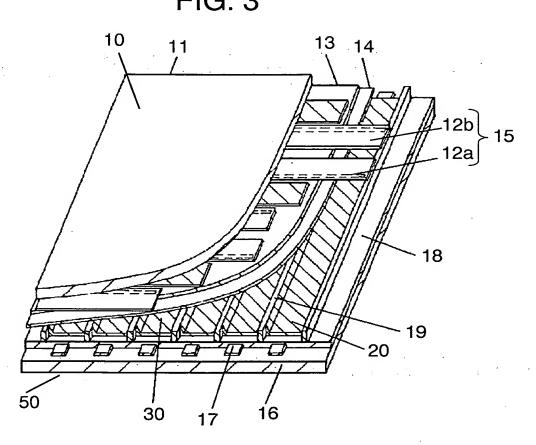
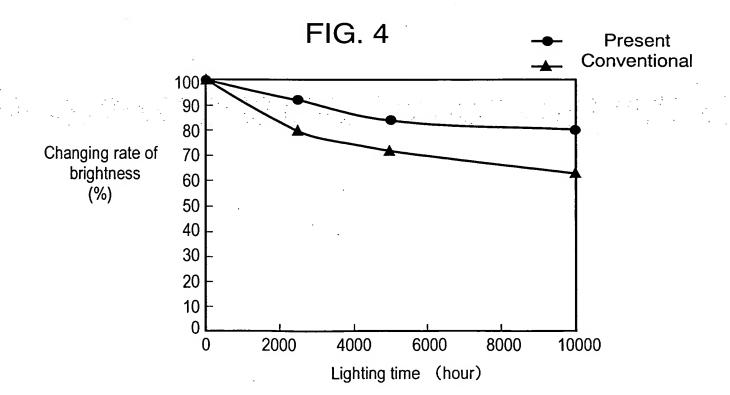


FIG. 2









## Reference marks in the drawings

10	front board
11	front substrate
12a	scan electrode
12b	sustain electrode
13	dielectric layer
14	protective layer
15	display electrode
16	rear substrate
17	data electrode
18	insulating layer
19	barrier rib
20	phosphor layer
30	discharge space
41	inlet
42	chamber
43	oxidizing atmosphere
44	alumina melting pot
45	phosphor powder
46	heating means
47	outlet
50	rear board